

COMPILER DESIGN (CEN603)

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Q1. The concept of Attributes.

→ Semantics or MEANING of a program can be viewed as computation of value of attributes associated with the non terminals in grammar.

→ Grammar symbols are associated with attributes (records of temporary variables in memory) to associate information with programming language constructs that they represent.

→ The values of these attributes are evaluated by semantic rules associated with the productions of grammar in syntax directed translation.

→ An attribute associated to variable can hold multiple types of information about that variable.

(i) A value : can be used in evaluation of expression

(ii) A pointer : can be used in translation to syntax trees or DAGs.

(iii) Type : For type checking and typecasting

(iv) A code : To translation into other forms of code like TAC (3-address codes)

(v) etc.

Eg. In following syntax directed definition of grammar for desk calculator.

PRODUCTION.

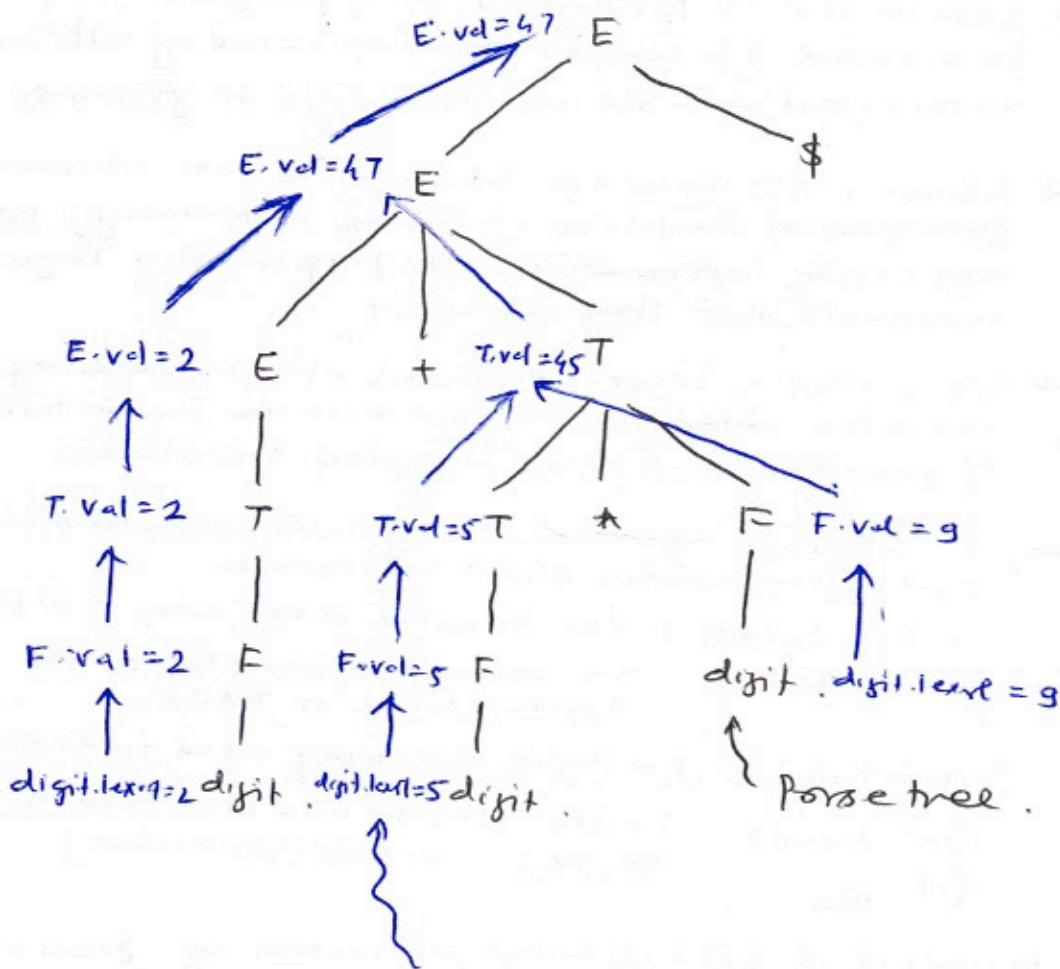
 $L \rightarrow E \$$ $E \rightarrow E_1 + T$ $E \rightarrow T$ $T \rightarrow T_1 * F$ $T \rightarrow F$ $F \rightarrow (E)$ $F \rightarrow \text{digit}$

SEMANTIC RULES.

 $\text{print}(E.\text{val})$ $E.\text{val} = E_1.\text{val} + T.\text{val}$ $E.\text{val} = T.\text{val}$ $T.\text{val} = T_1.\text{val} * F.\text{val}$ $T.\text{val} = F.\text{val}$ $F.\text{val} = E.\text{val}$ $F.\text{val} = \text{digit}.\text{lexval}$

Alongside parsing, we can also evaluate the input expression simultaneously by applying semantic rules at each stage and data flow among attributes at nodes in the parse tree.

eg Input : $2 + 5 * 9$.



dataflow graph among attributes associated to variables in parse tree for simultaneous evaluation of expression along with parsing

S-Attribute definitions

→ Based on synthesized attributes only.

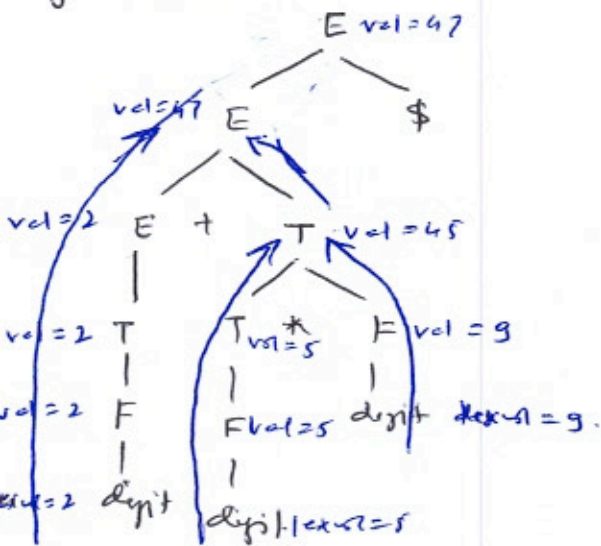
(data flows from children to parent only)

→ use bottom up parsing

→ Example

$L \rightarrow E\$$	$mu(E.val)$
$E \rightarrow E_1 + T$	$E.val = E_1.val + T.val$
$E \rightarrow T$	$E.val = T.val$
$T \rightarrow T_1 * F$	$T.val = T_1.val * F.val$
$T \rightarrow F$	$T.val = F.val$
$F \rightarrow (E)$	$F.val = E.val$
$F \rightarrow digit$	$F.val = digit.lexval$

eg. $2 + 5 * 9 \$$



Bottom up flow only.

L-Attribute definitions

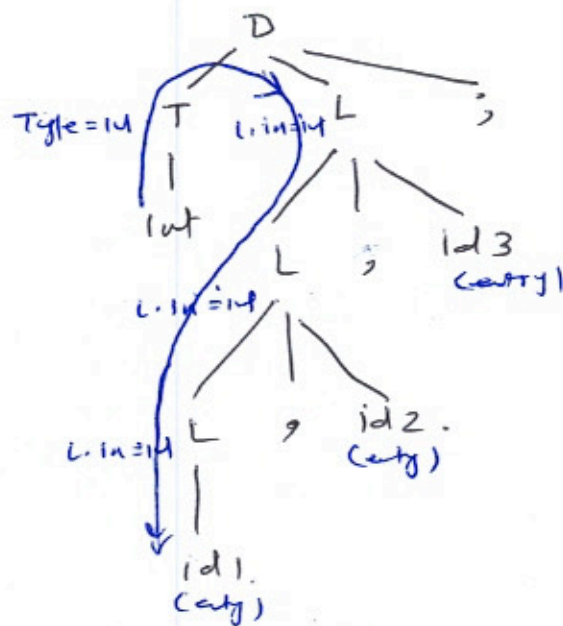
→ based on synthesized attributes as well as inherited attributes.

(data flows from children to parent or
or
data flows from parent to children
data flows from sibling to sibling
in the flow graph)

→ Arbitrary traversals or top-down parsing required to establish flow.

→ Example

$D \rightarrow TL ;$	$L.in = T.type$
$T \rightarrow int$	$T.type = int$
$T \rightarrow real$	$T.type = real$
$T \rightarrow class$	$T.type = class$
$L \rightarrow L_1 ; id$	$L.in = L_1.in$
$L \rightarrow id$	$L.in = id.lexval$



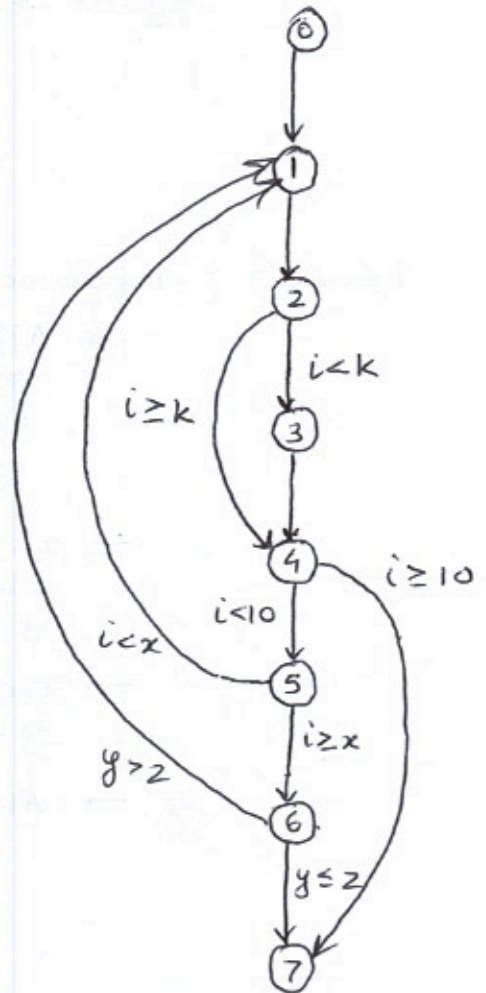
flow can be left to right
Top to bottom
or Bottom up.

Q2.

```
x = y * z;  
i = 0;  
do {  
    k = x + i;  
    i++;  
    if (i < k) {  
        a = a * b / k;  
    }  
} while (i < 10 && (i < x || y > z));
```

Three address code for above snippet. (Non optimized)

```
100 : t1 = y + z  
101 : x = t1.  
102 : i = 0  
103 : t2 = x + i  
104 : k = t2.  
105 : t4 = i + 1  
106 : i = t4.  
107 : if i < k goto 109.  
108 : goto 112  
109 : t5 = b / k  
110 : t6 = a * t5  
111 : a = t6.  
112 : if i < 10 goto 114  
113 : goto 118  
114 : if i < x goto 103.  
115 : goto 116.  
116 : if y > z goto 103.  
117 : goto 118  
118 : _____
```



Q3. various levels of compiler on which code can be optimized.

Level 1: Machine independent optimizations.

- These can be applied in frontend of compiler. i.e. LA, SA, SEMA, ICG.
 - These optimizations are inefficient to apply in LA, SA or SEMA passes and thus only attempted in intermediate code generation phase.
 - The sort of intermediate code which is imposed here does not involve any CPU registers or absolute memory locations.
- eg. → Broadly these optimizations are classified into
- Local optimizations, (peephole optimizations)
 - Loop optimizations.
 - Global optimizations.

Level 2: Machine dependent optimizations

- Applied in backend of compiler after target code is generated and it is being transformed into assembly code of target machine.
 - It involves CPU registers and may have absolute memory references rather than relative references.
 - These try to achieve maximum advantage of the memory hierarchy
- eg. → Aho Sethi Algorithm for reduction of spilling

Types of optimizations at ICG level.

1. Function preserving transformations.

1.1. common subexpression elimination.

$$\text{Common} \begin{pmatrix} t_1 = i+1 \\ t_2 = b[t_1] \\ t_3 = i+1 \\ a[t_3] = t_2 \end{pmatrix} \rightarrow \begin{pmatrix} t_1 = i+1 \\ t_2 = b[t_1] \\ a[t_1] = t_2 \end{pmatrix}$$

1.2 copy propagation.

1.3 dead code elimination

remove statements to which control never reaches.

if (false) ← never executes
 =
 =
 = → If its body can be removed

1.4. Constant folding.

$$A[i+1] = B[i+1] \rightarrow \begin{pmatrix} j = i+1 \\ A[j] = B[j] \end{pmatrix}$$

2. Loop optimizations

2.1 Code motion.

- Loop invariants placed ~~at~~ before loop.

for i = 1 to 1000
 j = 100 invariant
 k = i * j
 print k
 →
 j = 100
 for i = 1 to 1000
 k = i * j
 print k

90% - 10% rule
 10% of code take 90%
 of execution time due to
 loops.

2.2. Elimination of induction variables.

induction variable: one variable increases in AP series
 other variables increase in CP series.

2.3 Strength reduction

replacement of multiplication by a subtraction or addition
 must be possible when induction variables are modified
 in a loop.

$$\begin{pmatrix} j = j - 1 \\ t_2 = 4 * j \\ t_3 = a[t_2] \end{pmatrix} \rightarrow \begin{pmatrix} j = j - 1 \\ t = t_2 - 4 \\ t_3 = a[t_2] \end{pmatrix}$$

global optimizers

→ Building Directed acyclic data flow graphs to reuse common nodes.

$$x = a + b.$$

$$y = a + c$$

